

FIG. 2 (Prior Art)

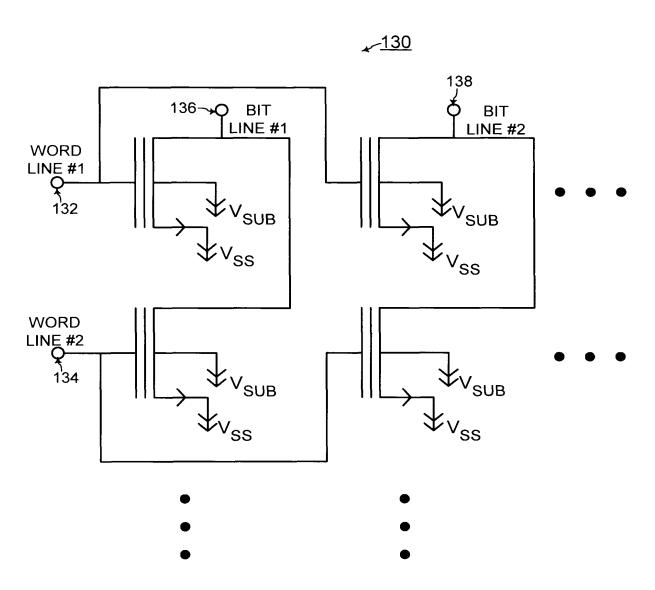
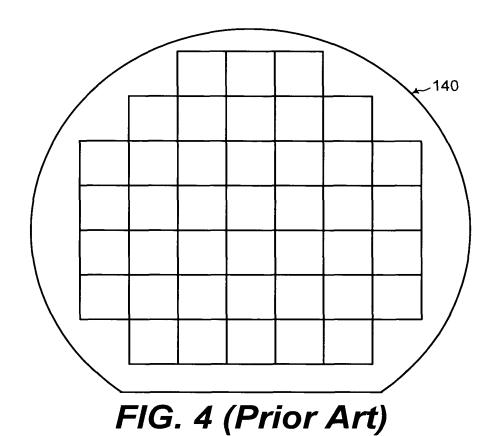


FIG. 3 (Prior Art)



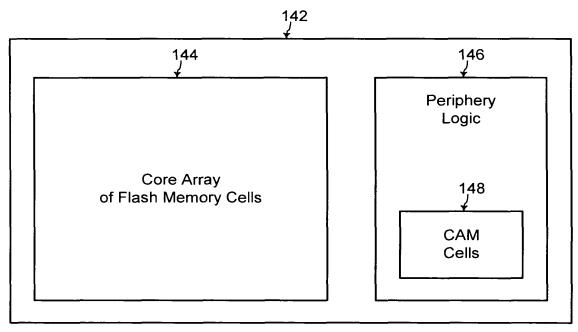
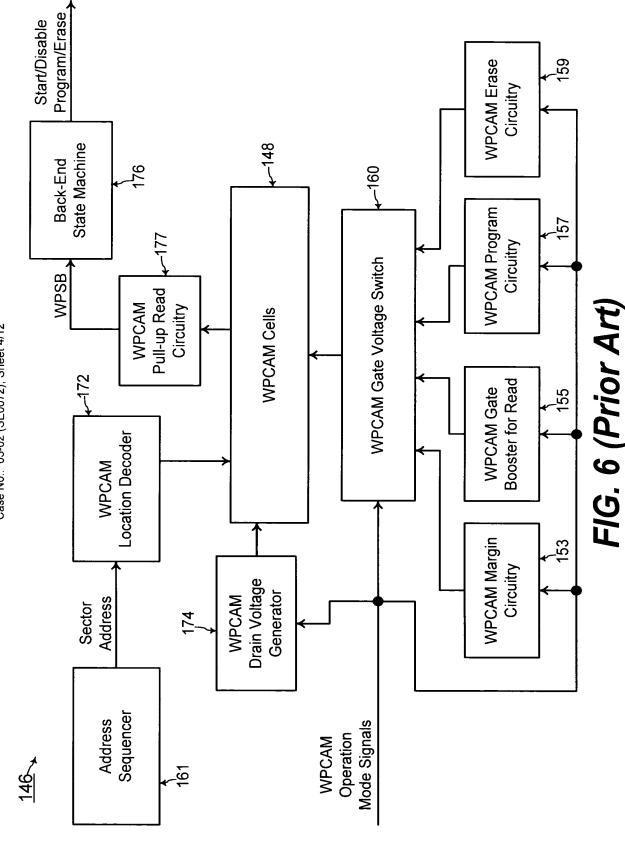


FIG. 5 (Prior Art)

Title: CAM (Content Addressable Memory) Cells as Part of Core Array in Flash Memory Device Inventor(s): Edward Bautista and Ken Cheong Cheah Case No.: 03-02 (SE0072), Sheet 4/12



Title: CAM (Content Addressable Memory) Cells as Part of Core Array in Flash Memory Device Inventor(s): Edward Bautista and Ken Cheong Cheah Case No.: 03-02 (SE0072), Sheet 5/12

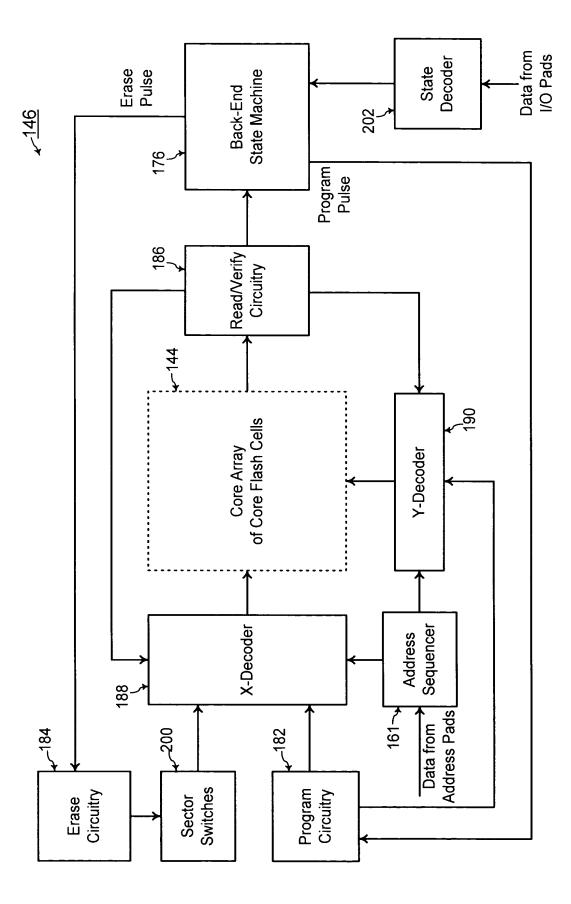
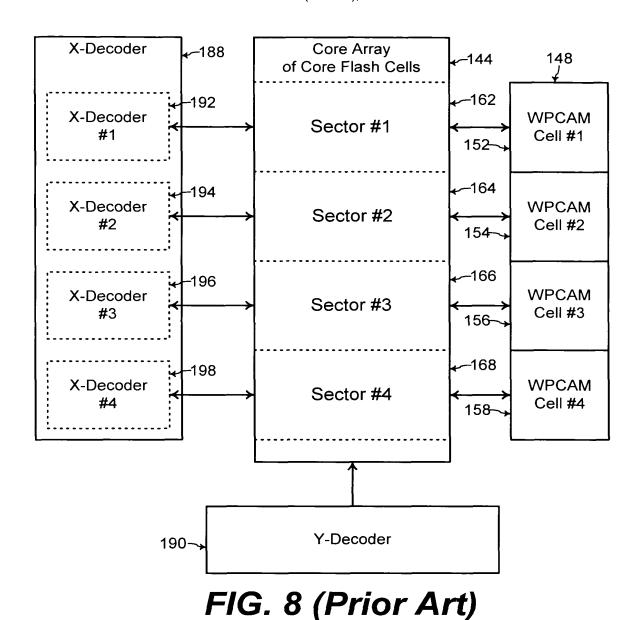


FIG. 7 (Prior Art)



170 169 167

Sector
Address bits
A[19:15]

X-Address bits
A[14:7]

X-Address bits
A[6:0]

FIG. 9 (Prior Art)

Title: CAM (Content Addressable Memory) Cells as Part of Core Array in Flash Memory Device Inventor(s): Edward Bautista and Ken Cheong Cheah Case No.: 03-02 (SE0072), Sheet 7/12

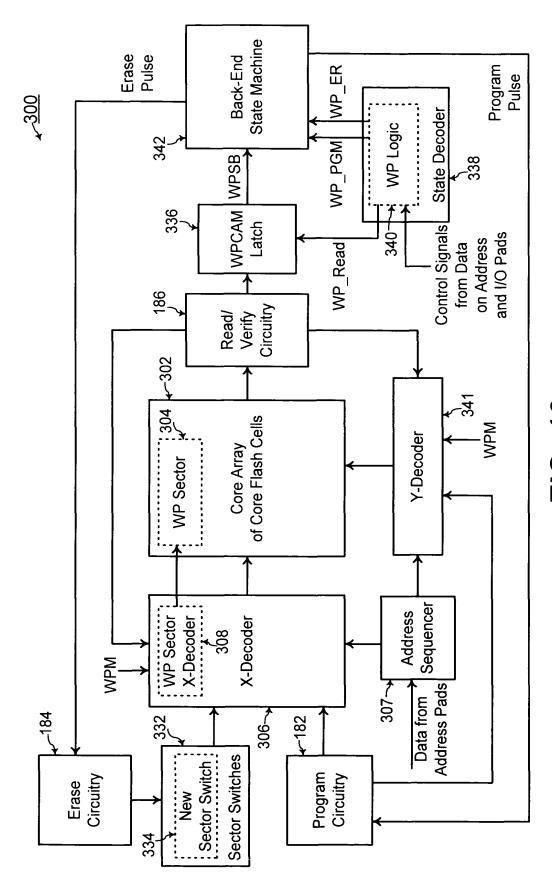
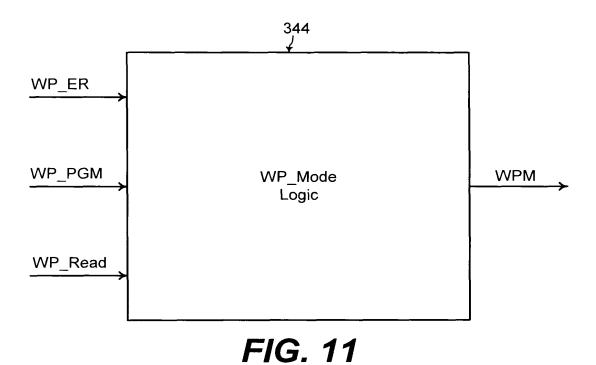
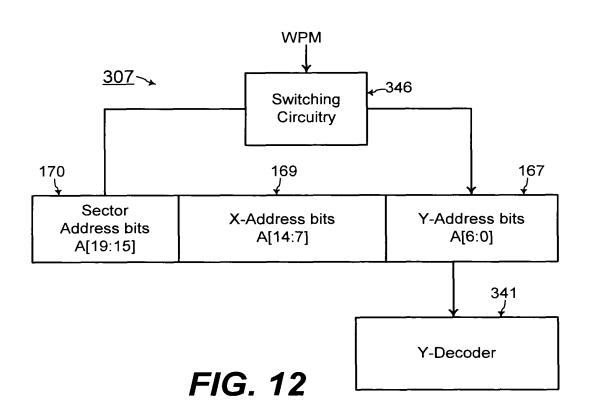
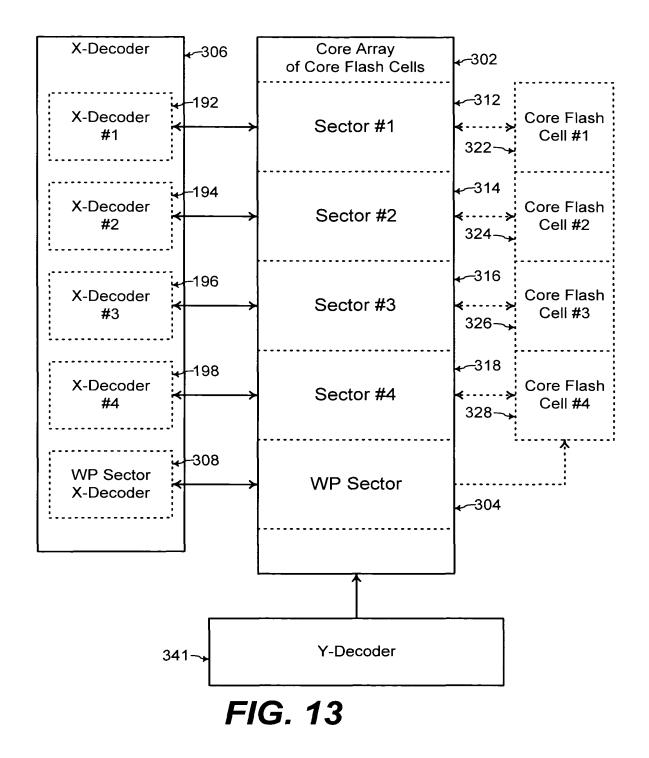


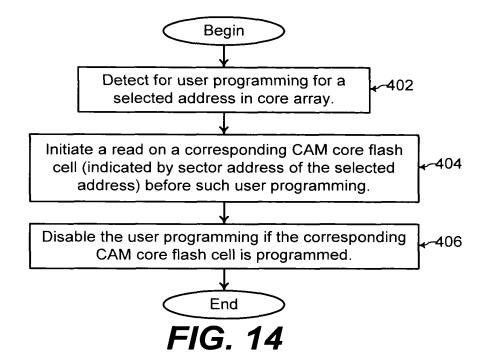
FIG. 10

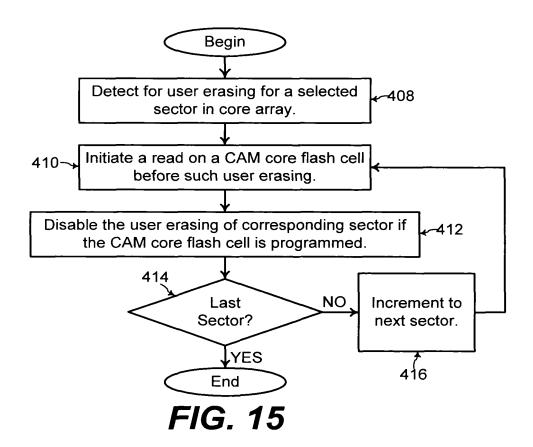




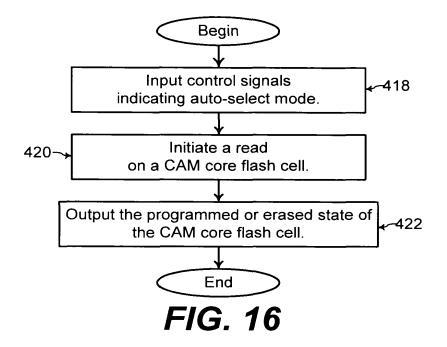


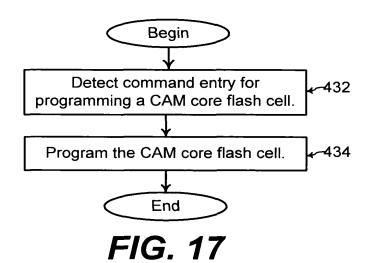
Title: CAM (Content Addressable Memory) Cells as Part of Core Array in Flash Memory Device Inventor(s): Edward Bautista and Ken Cheong Cheah
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